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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/715,288	11/17/2003	Gareth G. Hougham	YOR920030496US1	7006
7590	12/29/2005		EXAMINER	
			TSUKERMAN, LARISA Z	
			ART UNIT	PAPER NUMBER
			2833	
DATE MAILED: 12/29/2005				

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Please find below and/or attached an Office communication concerning this application or proceeding.

ATC

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/715,288	HOUGHAM ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Larisa Z. Tsukerman	2833	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on amendment dated 12/09/2005.  
 2e This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1,3-6 and 8-40 is/are pending in the application.  
 4a) Of the above claim(s) 11-40 is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1,3-6 and 8-10,41-42 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on 17 November 2003 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                     | Paper No(s)/Mail Date. _____ .  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____ .                                  |

***DETAILED ACTION***

***Drawings***

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the “voids” must be shown and numbered or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as “amended.” If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either “Replacement Sheet” or “New

Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Anselmo et al. (4381134).

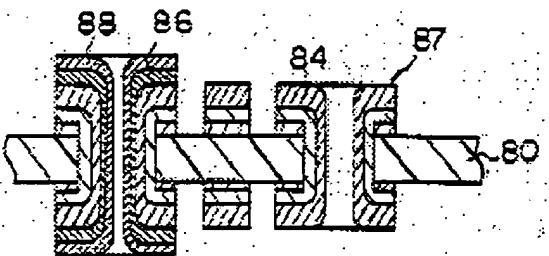
**In regard to claim 1,** Anselmo et al. disclose an interposer 11 comprising an electrically insulating carrier 11, an electrically conductive via 13 disposed in the carrier 11 and an electrical connector 20 that comprises a hollow body 23-26 of electrically conductive material that is disposed in electrical contact with the via 13 on opposite sides of the carrier 11 and that

has a plurality of surface voids (not marked, aria 25, 23 at the top and 31, 23 at the button, see Figs. 2 – 3) so that the electrical connector 20 accommodates forced physical and electrical contact with at least one separate electrical contact (see Abstract, lines 4-5).

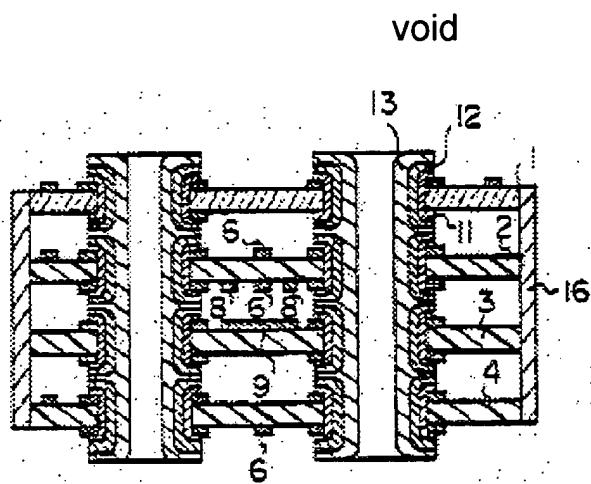
Claims 1, 4 – 5 are rejected under 35 U.S.C. 102(b) as being anticipated by Kurosawa et al. (4528072).

**In regard to claim 1,** Kurosawa et al. disclose an interposer (not marked, see Attachment 1) comprising an electrically insulating carrier 80, an electrically conductive via 10 (Fig.1C) disposed in the carrier 80 and an electrical connector 84, 87 that comprises a hollow body (not marked) of electrically conductive material that is disposed in electrical contact with the via 10 on opposite sides of the carrier 80 (see Fig. 9D) and that has a plurality of surface voids V (see Attachment 1) so that the electrical connector 20 accommodates forced physical and electrical contact with at least one separate electrical contact.

void



Attachment 1

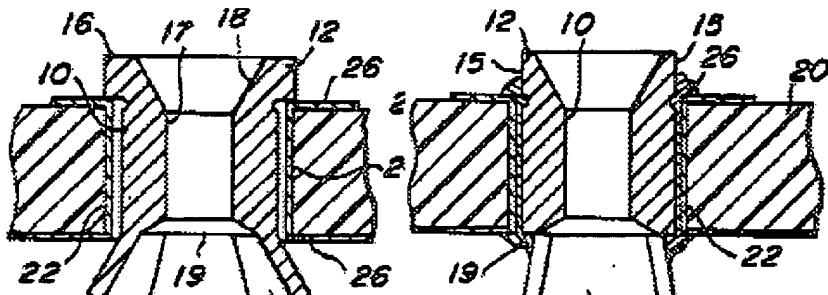


**In regard to claim 4**, Kurosawa et al. disclose the hollow body 86, 88 comprises at least two voids V (see Attachment 1) in registration with one another and disposed on opposite sides of the carrier 80.

**In regard to claim 5**, Kurosawa et al. disclose the carrier 80 comprises a plurality of the electrically conductive vias V (see Attachment 1) each disposed in electrical contact with a different one of a plurality of the hollow bodies 86/88.

Claims 1, 4 - 5, and 41 are rejected under 35 U.S.C. 102(b) as being anticipated by DeSantis et al. (4181385).

**In regard to claim 1**, DeSantis et al. disclose an interposer (not marked, see Attachment 2) comprising an electrically insulating carrier 20, an electrically conductive via 22 disposed in the carrier 20 and an electrical connector (socket) 12,10,14 that comprises a hollow body 10 of electrically conductive material that is disposed in electrical contact with the via 22 on opposite sides of the carrier 20 (see Figs. 3 - 4) and that has a plurality of surface voids 18 so that the electrical connector 12, 10, 14 accommodates forced physical and electrical contact with at least one separate electrical contact.



Attachment 2

**In regard to claim 4**, DeSantis et al. disclose at least two of the surface voids 18 are in registration with one another and disposed on opposite sides of the carrier 20.

**In regard to claim 5**, DeSantis et al. disclose the carrier 20 comprises a plurality of the electrically conductive vias 22 each disposed in electrical contact with a different one of a plurality of the hollow bodies 10.

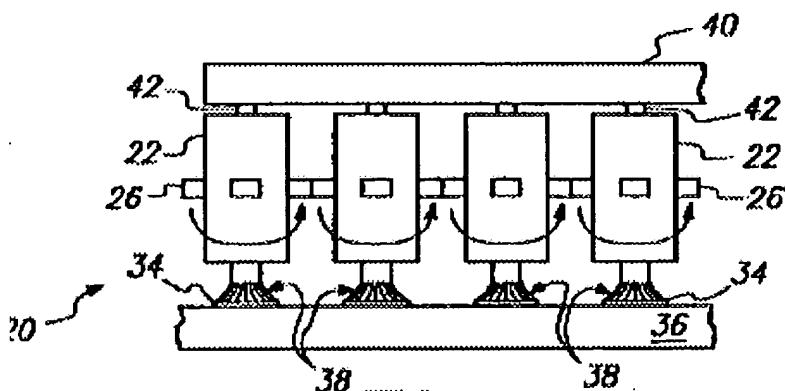
**In regard to claim 41**, DeSantis et al. disclose at least one of the voids is located to receive an optical signal so that the hollow body 10 is capable to accommodate both optical and electrical signals.

Claims 6 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Matsuba et al. (5562462).

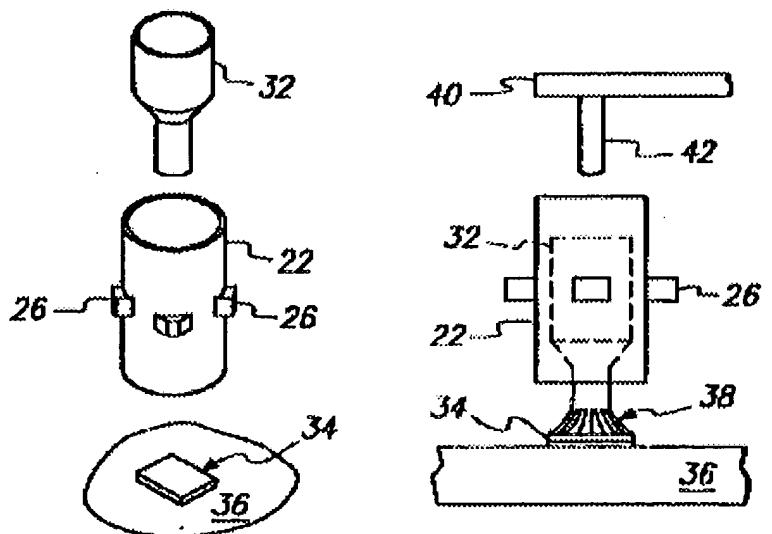
**In regard to claim 6**, Matsuba et al. disclose an interposer (not marked, see Attachment 4) for connecting a module 40 to a printing wiring board 36, comprising:

a carrier 20 that has at least one electrically conductive via (not marked, see Col.6, line 67 and Col.7, lines 1-3) and that is disposed so that the via is in registration with a connector 42 of the module 40 and a connector 34 of the printed wiring board 36, and at least one electrical contact button 32 that is hollow (see Fig. 4), that is disposed in the via (see Col.6, line 67 and Col.7, lines 1-3) for forced physical and electrical contact with the connector 42 of the module 40 and the connector 38 of the printed

wiring board 36 and that has at least one surface void V (not marked, see Attachment 4).



Attachment 4



In regard to claim 10, Matsuba et al. disclose the carrier 20 comprises a plurality of the vias (not marked, see Col.6, line 67 and Col.7, lines 1-3) in which a plurality of the electrical contact buttons 32 is disposed (see Fig.7).

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuba et al. (5562462) in view of Patrow (4902606).

In regard to claim 3, Matsuba et al. disclose most of the claimed invention except for the electrical contact button comprises a plurality of surface voids, at least two of which are disposed on one side of the carrier.

However, Patrow teaches a compressive connectors 20 having an electrical contact button comprises a plurality of surface voids, at least two of which are disposed on one side of the carrier of voids (see Fig.1f) to form a spring loaded mechanical and electrical interconnection. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to include a plurality of surface voids, at least two of which are disposed on one side of the carrier of voids in structure of

Matsuba et al., as taught by Patrow, in order to form a spring loaded mechanical and electrical interconnection.

Claim 6, 9 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Anselmo et al. (4381134) in view of Matsuba et al. (5562462).

**In regard to claim 6,** Anselmo et al. disclose an interposer (not marked, see Attachment 5) (*for connecting a module 40 to a printing wiring board 36*), comprising:

a carrier 11 that has at least one electrically conductive via 13 and that is disposed so that the via 13 is in registration (*with a connector of the module and a connector of the printed wiring board*), and at least one electrical contact button 20 that is hollow (see Fig. 2), that is disposed in the via 13 (*for forces physical and electrical contact with the connector of the module*) and the connector of the printed wiring board and that has at least one surface void V (not marked, see Attachment 5). However, Anselmo et al. is silent about what kind of conductive elements connector 20 connects.

Matsuba et al. (5562462) teach an interposer 20,22 that interconnects a module 40 with PCB 36. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to include

the connector 20 for connection between the module 40 and the PCB 36 as taught by Matsuba et al. in structure of et al. based on intended use reason.

**In regard to claim 9,** Anselmo et al. modified by Matsuba et al. disclose the electrical contact button 20 comprises at least two surface voids (not marked, aria 25, 23 at the top and 31, 23 at the button, see Figs. 2 – 3) in registration with one another and disposed on opposite sides of the carrier 11.

**In regard to claim 10,** Anselmo et al. modified by Matsuba et al. disclose the carrier 11 comprises a plurality of the vias 13 in which a plurality of the electrical contact buttons 20 is disposed (see Fig.1).

Claims 3 and 42 are rejected under 35 U.S.C. 103(a) as being unpatentable over DeSantis et al. (4181385) in view of Patrow (4902606).

**In regard to claim 3,** DeSantis et al. disclose most of the claimed invention except for the electrical contact button comprises a plurality of surface voids, at least two of which are disposed on one side of the carrier.

However, Patrow teaches a compressive connectors 20 having an electrical contact button comprises a plurality of surface voids, at least two of which are disposed on one side of the carrier of voids (see Fig.1f) to

form a spring loaded mechanical and electrical interconnection. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to include a plurality of surface voids, at least two of which are disposed on one side of the carrier of voids in structure of DeSantis et al., as taught by Patrow, in order to form a spring loaded mechanical and electrical interconnection.

**In regard to claim 42,** DeSantis et al. disclose most of the claimed invention except for the plurality of voids is arranged in a birdcage pattern. However, Patrow teaches a compressive connectors 20 having a plurality of voids (see Fig.1f) arranged in a birdcage pattern to form a spring loaded mechanical and electrical interconnection. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to include the plurality of voids is arranged in a birdcage pattern in structure of DeSantis et al., as taught by Patrow, in order to form a spring loaded mechanical and electrical interconnection.

***Response to Arguments***

Applicant's arguments, filed on 12/09/2005, with respect to claims 1 – 10, have been considered but are moot in view of the new ground(s) of rejection.

Regarding new issue added to claims 1 and 6 that "electrical connector accommodates forced physical and electrical contact with at least one separate electrical contact", Examiner considers that all used references perform that function.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Larisa Z. Tsukerman whose telephone number is (571)-272-2015. The examiner can normally be reached on Monday through Friday from 8:30 am to 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on (571)-272-2800 ex. 33. The fax phone number for the organization where this application or proceeding is assigned is (571)-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status

information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LT,12/27/2005

ROSS GUERRI  
PRIMARY EXAMINER  
